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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/676,067	10/02/2003	Yojiro Matsueda	117390	7780

25944 7590 12/01/2006

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ALEXANDRIA, VA 22320

EXAMINER

DUONG, DIEU HIEN

ART UNIT	PAPER NUMBER
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2821

DATE MAILED: 12/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/676,067

Applicant(s)

MATSUEDA ET AL.

Examiner

Dieu Hien T. Duong

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 November 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 October 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>11/10/03, 01/27/06</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

The Preliminary Amendment filed November 10, 2003 is acknowledged.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Yamazaki et al (US 6,825,820).

Regarding claim 1, Figures 14A and 14B of Yamazaki disclose an electro-optical device comprising an electric power supply circuit 904; and a plurality of pixels 906 disposed in the form of a matrix including electro-optical devices driven by receiving electric power from the electric power supply circuit; the plurality of pixels making up a plurality of pixel groups formed of a series of pixels arrayed in at least one direction of the row direction and the column direction, and line forming regions 914r, 912g, 913 being formed between adjacent pixel groups of the plurality of pixel groups, and line forming regions being formed with generally the same width.

Regarding claim 2, Figures 14A and 14B of Yamazaki disclose an electro-optical device comprising a plurality of scan lines 913; a plurality of data lines 914r, 912g, 912b;

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a plurality of pixels 906, disposed at portions corresponding to intersections of the scan lines and the data lines, including electro-optical devices; and a plurality of electric power lines 914r, 914g, 914b to supply driving voltage to the electro-optical devices; the plurality of pixels making up a plurality of pixel groups formed of a series of pixels arrayed in at least one direction of the row direction and the column direction, a plurality of line forming regions 914r, 912g being formed between adjacent pixel groups of said plurality of groups, and at least two lines selected from at least one electric power line 914r of the plurality of electric power lines, at least one scan line 913 of the plurality of scan lines, and at least one data line 912g of the plurality of data lines, being formed in at least one line forming region of the plurality of line forming regions.

Claims 3-4 are rejected for similar subject matter to claim 2.

Regarding claim 5, as applied to claim 2, Figure 14A and 14B of Yamazaki disclose the line forming regions being formed with generally the same width.

Regarding claim 6, as applied to claim 1, Figure 1B and page 6, lines 5-15 of Yamazaki discloses the electro-optical devices being operated with each different driving voltages; and the electric power lines to supply voltage to the electro-optical devices being formed with different widths corresponding to the driving voltage.

Regarding claim 7, as applied to claim 6, Figure 1B of Yamazaki discloses the electro-optical device being a light-emission device; and the electric power lines being formed with different widths corresponding to the emission light color of said light-emission device.

Regarding claim 8, as applied to claim 7, Figure 1B of Yamazaki discloses the color of the light which is to be emitted being at least one of red, green, and blue.

Regarding claim 9, as applied to claim 1, Yamazaki (col. 23, lines 21-35) discloses the electro-optical device being an electro-luminescence device.

Regarding claim 10, as applied to claim 1, Figure 12 of Yamazaki discloses an electronic apparatus comprising the electro-optical device according to claim 1.

Regarding claim 11, Figures 14A and 14B of Yamazaki disclose a matrix substrate comprising a plurality of pixel electrodes 906 disposed in the form of a matrix, the plurality of pixel electrodes making up a plurality of pixel electrode groups formed of a series of pixel electrodes arrayed in at least one direction of the row direction and the column direction, and a plurality of line forming regions being formed between adjacent pixel electrode groups of the plurality of pixel electrode groups and the line forming regions being formed with generally the same width.

Regarding claim 12, Figures 14A and 14B of Yamazaki disclose a matrix substrate comprising a plurality of scan lines 913; a plurality of data lines 914r, 912g, 912b; a plurality of pixel electrodes 906 disposed at portions corresponding to intersections of the scan lines and the data lines; and a plurality of electric power lines 914r, 914g, 914b to supply voltage to the plurality of pixel electrodes; the plurality of pixel electrodes make-making up a plurality of pixel electrode groups formed of a series of pixel electrodes arrayed in at least one direction of the row direction and the column direction, a plurality of line forming regions are formed between adjacent pixel electrode groups of the plurality of pixel electrode groups, and at least two lines selected from at

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least one electric power line 914r of the plurality of electric power lines, at least one scan line 913 of the plurality of scan lines, and at least one data line 912g of the plurality of data lines, being formed in at least one line forming region of the plurality of line forming regions.


Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dieu Hien T. Duong whose telephone number is 571-272-8980. The examiner can normally be reached on Monday - Friday, from 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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DD**HOANG V. NGUYEN**
PRIMARY EXAMINER